

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshihiro HASHIMOTO Art Unit :
Serial No.: Examiner :
Filed :
Title : A CONSTANT VOLTAGE SOURCE, A CONSTANT VOLTAGE SOURCE
CIRCUIT BOARD AND A METHOD FOR APPLYING A CONSTANT
VOLTAGE

Assistant Commissioner for Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to consideration of the above application, please amend the application as follows and consider the included remarks.

IN THE CLAIMS:

Please amend the claims as shown below. Marked-up versions of the amended claims are attached.

10. A constant voltage source as claimed in claim 8, wherein inductances between respective said first and second bypass capacitors and said load are inductances of wiring lines between said first and second bypass capacitors and said load,

and said first and second inductance units are wiring lines.

18. A constant voltage source as claimed in claim 16, wherein inductances between respective said first, second and third bypass capacitors and said load are inductances of respective wiring lines between said first, second and third bypass capacitors and said load, and

said first, second and third inductance units are wiring lines.



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24. A constant voltage source circuit board as claimed in claim 22, wherein inductances between respective said first and second bypass capacitors and said load are respectively inductances of wiring lines between said first and second bypass capacitors and said load, and said first and second inductance units are wiring lines.

33. A constant voltage source circuit board as claimed in claim 19, wherein at least one of said first, second and third bypass capacitors is disposed around said load.

34. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of said wiring line is disposed around said load.

35. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of at least one of wiring lines between respective said first, second and third bypass capacitors and said load is piled over other wiring line.

37. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of at least one of wiring lines between respective said first, second and third bypass capacitors and said load is formed to pile up over at least one of said first, second and third bypass capacitors.

39. A constant voltage source circuit board as claimed in claim 22, wherein at least a portion of at least one of wiring lines between respective of said first, second and third bypass capacitors and said load is formed to pile up over said load.

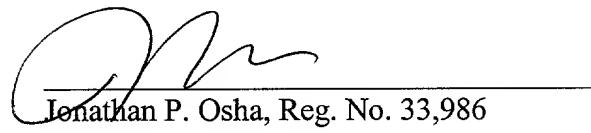
REMARKS

Please reconsider the application in view of the above amendments and the following remarks. The claims have been amended solely to remove multiple dependencies. This removal is made both to comply with 37 C.F.R. § 1.75 and to reduce the additional costs of multiple dependent claims. These amendments are in no way related to issues of patentability.

Please apply any charges not covered, or any credits, to Deposit Account 50-0591
(Reference Number 02008.069001).

Respectfully submitted,

Date: 4/10/01



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Marked-Up Version of the Claims

IN THE CLAIMS:

10. A constant voltage source as claimed in claim 8 [or claim 9], wherein inductances between respective said first and second bypass capacitors and said load are inductances of wiring lines between said first and second bypass capacitors and said load,

and said first and second inductance units are wiring lines.

18. A constant voltage source as claimed in claim 16 [or claim 17], wherein inductances between respective said first, second and third bypass capacitors and said load are inductances of respective wiring lines between said first, second and third bypass capacitors and said load, and said first, second and third inductance units are wiring lines.

24. A constant voltage source circuit board as claimed in claim 22 [or claim 23], wherein inductances between respective said first and second bypass capacitors and said load are respectively inductances of wiring lines between said first and second bypass capacitors and said load,

and said first and second inductance units are wiring lines.

33. A constant voltage source circuit board as claimed in [any one of claims 19, 21 and 27] claim 19, wherein at least one of said first, second and third bypass capacitors is disposed around said load.

34. A constant voltage source circuit board as claimed in [any one of claims 22 to 24, 28 and 30] claim 22, wherein at least a portion of said wiring line is disposed around said load.

35. A constant voltage source circuit board as claimed in [any one of claims 22 to 24, 28 and 30] claim 22, wherein at least a portion of at least one of wiring lines between respective said first, second and third bypass capacitors and said load is piled over other wiring line.

37. A constant voltage source circuit board as claimed in [any one of claims 22 to 24, 28 and 30] claim 22, wherein at least a portion of at least one of wiring lines between respective said first, second and third bypass capacitors and said load is formed to pile up over at least one of said first, second and third bypass capacitors.

39. A constant voltage source circuit board as claimed in [any one of claims 22 to 24, 28 and 30] claim 22, wherein at least a portion of at least one of wiring lines between respective of said first, second and third bypass capacitors and said load is formed to pile up over said load.